

# ASANKA SOVIS

## PERSONAL INFORMATION

Born	Sri Lanka, 12 <sup>th</sup> September 1998
Email & Social	<a href="mailto:contact@asankasovis.com">contact@asankasovis.com</a> · <a href="#">LinkedIn</a> · <a href="#">Github</a>
Goal	Electronics engineering graduate with experience in Embedded Engineering, currently working at Zebra Technologies as a Software Engineer, seeking further opportunities.

## WORK EXPERIENCE

<i>Zebra Technologies, Colombo</i>	<i>Oct 2024 - Present</i> Software Engineer, I (Grade 10), ZEBRA <ul style="list-style-type: none"><li>· Design, develop &amp; test a variety of software assignments that constitute a portion of a larger project.</li><li>· Develop &amp; test documentation which describes the software functionality.</li><li>· Run &amp; review software tests designed by more experienced engineers.</li></ul>
<i>SenzMate IoT Intelligence, Colombo</i>	<i>Jun 2024 - Oct 2024</i> Embedded Engineer, SENZMATE <i>Dec 2023 - May 2024</i> Associate Embedded Engineer, SENZMATE <ul style="list-style-type: none"><li>· Researched, designed, implemented, managed &amp; tested embedded systems including firmware &amp; automation scripts.</li><li>· Identified areas of modification &amp; wrote efficient code bases.</li><li>· Brainstormed ideas with team members &amp; guided junior team members.</li></ul>
<i>Atlas Labs, Colombo</i>	<i>May 2023 - Nov 2023</i> Embedded Engineer - Intern, ATLAS LABS <ul style="list-style-type: none"><li>· Fulfilled the requirements of the degree program as a trainee engineer.</li><li>· Part of the team that completed the <a href="#">Krakatoa Ko.5 project</a>.</li><li>· Developed test firmware, soldered &amp; validated PCBs, &amp; manufactured, bench-tested &amp; debugged sub-assemblies including the prototypes themselves.</li></ul>

## EDUCATIONAL QUALIFICATIONS

<i>BSc. (Hons) in Engineering in Electronics &amp; Telecommunica- tions</i>	<i>Apr 2019 - Nov 2023</i> SLTC Research University, Padukka <i>School: School of Engineering</i> Description: Focused on electronics engineering while specializing in telecommunication engineering. In addition to theoretical knowledge; special focus on self-learning & project-based experience. <i>Research Project: <a href="#">Project Almanac</a></i>
<i>School Education</i>	<i>Jan 2004 - Sep 2018</i> Maris Stella College, Negombo

## CERTIFICATIONS

<i>BerkeleyX on edX Online</i>	<i>Dec 2021</i> · Professional Certificate for Blockchain Fundamentals <a href="#">Certification Link</a>
<i>MITX on edX Online</i>	<i>Feb 2020</i> · Verified Certificate for Introduction to Computer Science & Programming Using Python <a href="#">Certification Link</a>

## SKILLS & EXPERTISE

<i>Technical Skills</i>	PCB Design, Firmware Development, Hardware Design & FPGA, Embedded Linux, Zephyr & other RTOS, Systems Integration, QNX Development
<i>Platforms</i>	AMD XILINX, INTEL QUARTUS, ATMEL, ARDUINO, STM32, ESP32, NORDIC NRF
<i>Languages</i>	C/C++, ASSEMBLY, PYTHON, BASH, JAVA, JAVASCRIPT, VISUAL C#, RUST, PHP
<i>Extra Skills</i>	Blockchain Technology, Reverse Engineering, Cybersecurity, Agile Development, Networking, Project Management, Documentation, $\LaTeX$

## PROFESSIONAL PROFILE

<i>Portfolio</i>	<ul style="list-style-type: none"><li>· Final year project on developing an optimized timetable scheduling system using machine learning, cluster computing &amp; high-performance computing: <a href="#">Project Almanac</a>.</li><li>· Other noteworthy projects including <a href="#">Indoor Positioning System using FPGA</a>, <a href="#">Prometheus: Smart Fire Alarm System</a>, <a href="#">Metroparking: Smart Parking System</a>, <a href="#">Muragala Password Manager</a> &amp; <a href="#">Akaradiya: Opensource Sinhala Dictionary</a>.</li><li>· Maintain blogs related to programming &amp; electronics on <a href="#">Blogger</a> &amp; <a href="#">Hashnode</a>.</li></ul>
<i>Publications</i>	<ul style="list-style-type: none"><li>· <a href="#">Enhanced Timetable Scheduling: A High-Performance Computational Approach</a> — Publisher: IEEE</li></ul>
<i>Achievements</i>	<ul style="list-style-type: none"><li>· One of the top 100 submissions of the <a href="#">Make it Matter</a>, 2023 competition held by Nordic Semiconductor on <a href="#">hackster.io</a>, received by the <a href="#">Prometheus Fire Alarm System v2.0</a>.</li><li>· Completed the Aquinas Diploma in English Language &amp; Literature with Distinction Pass in 2019.</li><li>· Represented the school in the Sri Lanka Robotics Championship (SLRC) organized by the E-Club of the University of Moratuwa in 2016.</li></ul>
<i>Organizations</i>	<ul style="list-style-type: none"><li>· Former member of the Students for the Exploration &amp; Development of Space (SEDS) club of SLTC Research University since 2020.</li><li>· Former member of the University Student Enterprise (USE) club of SLTC Research University since 2020.</li></ul>
<i>Languages</i>	SINHALA · Mothertongue ENGLISH · Fluent (Conversationally & Literary)
<i>Interests</i>	Guitar · Workout · Swimming · Blogging · Tinkering with Electronics

## REFERENCE

<i>Yohan Pandigama</i>	Founder - C2L3 Former Lecturer (CS) - SLTC Research University, Padukka. Former HoD - IT - CINEC Campus, Malabe. MIEEE, MIEEE - CS, MACM, MBCS, MCS (SL), MCS, BIT <a href="mailto:yohanp@sltc.ac.lk">yohanp@sltc.ac.lk</a> · +94 71 300 8883
<i>Lasith Premaratne</i>	Senior Embedded Engineer - Atlas Labs, Sri Lanka. B.Eng Swinburne University of Technology, Australia. <a href="mailto:lasithishan@gmail.com">lasithishan@gmail.com</a> · +94 77 68 53 610